

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A frequency regulating circuit for the current-consumption-dependent clock supply of a circuit configuration, comprising:

a current measuring device for measuring an instantaneous current consumption of the circuit configuration;

means for comparing the instantaneous current measured by said current measuring device with a definable threshold value;

a controllable clock supply circuit having:

an output to be connected to a clock input of the circuit configuration; and

a clock generator generating a clock signal with clock pulses at said output; said clock generator generating a constant maximum internal frequency; and

a pulse filter for filtering clock pulses from said clock signal from said clock generator, said pulse filter

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including a control input, a filtered clock signal being
provided to said output;

a control device connected to said clock supply circuit and driving said clock supply circuit based upon the measured current consumption, said control device providing a control signal to said control input of said pulse filter~~controlling said clock supply circuit to filter out individual clock pulses of said clock signal and reduce a clock frequency at said output of said clock supply circuit~~ when said means for comparing determine that the instantaneous current consumption exceeds the definable threshold value; and

said pulse filter suppressing an individual clock pulse of said clock signal generated by said clock signal generator, in response to said control signal at said control input, such that said control device~~adjusting~~ adjusts said clock frequency to provide at said output, at any time, the maximum possible clock frequency corresponding to a maximum permissible current consumption of the circuit.

Claim 2 (canceled)

Claim 3 (previously presented): The frequency regulating circuit according to claim 1, wherein said means for comparing

further comprise a comparator comparing the current measured by the current measuring device with the definable threshold value.

Claim 4 (currently amended): A frequency regulating circuit for the current-consumption-dependent clock supply of a circuit configuration, comprising:

a current measuring device for measuring an instantaneous current consumption of the circuit configuration;

means for comparing the instantaneous current measured by said current measuring device with a definable threshold value;

a controllable clock supply circuit having:

an output to be connected to a clock input of the circuit configuration; ~~and~~

a clock generator generating a clock signal with clock ~~pulses at said output~~, said clock generator generating a constant maximum internal frequency; and

a pulse filter connected to at least one of said clock generator and said output, for filtering clock pulses

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from said clock signal from said clock generator, said
pulse filter including a control input, a filtered clock
signal being provided to said output;

a control device connected to said clock supply circuit and driving said clock supply circuit based upon the measured current consumption, said control device programmed to ~~control~~ ~~said clock supply circuit by filtering out individual clock pulses of said clock signal to reduce a clock frequency at~~ said output of said clock supply circuit provide a control signal to said control input of said pulse filter when said means for comparing determines that the instantaneous current consumption exceeds the definable threshold value; and

said pulse filter suppressing an individual clock pulse of
said clock signal generated by said clock signal generator, in
response to said control signal at said control input, such
that, said control device adjustingadjusts said clock frequency to provide at said output, at any time, the maximum possible clock frequency corresponding to a maximum permissible current consumption of the circuit.

Claim 5 (canceled):

Claim 6 (previously presented): The frequency regulating circuit according to claim 4, wherein said means for comparing comprise a comparator comparing the current measured by the current measuring device with a definable threshold value.

Claim 7 (currently amended): A frequency regulating circuit for the current-consumption-dependent clock supply of a circuit configuration, comprising:

a current measuring device for measuring an instantaneous current consumption of the circuit configuration;

means for comparing the instantaneous current measured by said current measuring device with a definable threshold value;

a controllable clock supply circuit having:

an output to be connected to a clock input of the circuit configuration; and

a clock generator generating a clock signal with clock pulses ~~at said output~~, said clock generator generating a constant maximum internal frequency; and

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a pulse filter connected between said clock generator and
said output, said pulse filter including a control input;

a control device connected to said clock supply circuit and driving said clock supply circuit based upon the measured current consumption, said control device programmed to provide a control signal to said control input of said pulse filter
~~filter out individual clock pulses of said clock signal for reducing a clock frequency at said output of said clock supply circuit~~ when said means for comparing determine that the instantaneous current consumption exceeds the definable threshold value; and

said pulse filter filtering out individual clock pulses of said clock signal generated by said clock signal generator, in response to said control signal at said control input, such that, said control device adjustingadjusts said clock frequency to provide at said output, at any time, the maximum possible clock frequency corresponding to a maximum permissible current consumption of the circuit.

Claim 8 (canceled)

Claim 9 (previously presented): The frequency regulating circuit according to claim 7, wherein said means for comparing

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comprise a comparator comparing the current measured by the
current measuring device with a definable threshold value.